## **UNIVERSITY OF KERALA**

## TIMETABLE FOR 2018 SCHEME B.TECH DEGREE PRACTICAL EXAMINATION FOR COMPUTER SCIENCE & ENGINEERING (SEMESTER III, NOVEMBER 2022) & ELECTRONICS & COMMUNICATION ENGINEERING (SEMESTER IV, NOVEMBER 2022 & SEMESTER V, JANUARY 2023)

## UNIVERSITY COLLEGE OF ENGINEERING KARIAVATTOM (2018 & 2019 ADMISSION CANDIDATES)

Course	Semester	Subject	Date	Centre
Computer Science & Engineering (415)	November	415: 18308: Programming Lab	13.02.2023	University College of Engineering, Kariyavattom
Electronics & Communication Engineering (412)	Semester IV	412: 18407: Logic Circuit Design Lab	13.02.2023	
		412: 18408: Analog Inetgrated Circuits Lab		
	Semester V January 2023	412: 18507: Communication Engineering Lab	16.02.2023	

Thiruvananthapuram Dated 10.02.2023

Sd/-Controller of Examinations